







ICEC2NT 2025

IEEE International Conference on Electronics, Computing and Communication Networking Automation Technologies 2025

3-4 SEPTEMBER 2025

HYBRID MODE

Hosted By D.Y. Patil College of Engineering, Pune

ABOUT THE COLLEGE

Dr. D. Y. Patil Prathishthan's, D.Y. Patil College of Engineering, was established in 1984 in Pimpri and later shifted to Akurdi complex in 2001,which is in the vicinity of PimpriChinchwad Industrial area, one of the biggest Industrial belts in Asia. This Institute is approved by AICTE, New Delhi and is affiliated to the SavitribaiPhule Pune University. D Y Patil College of Engineering has been granted Autonomous status from the Academic Year 2024-25. Institute is accrediated by NAAC & Programme are accrediated by NBA.

The college has excellent & ambient infrastructure with well-equipped laboratories. Well-qualified, motivated, and dedicated faculty members are serving in the Institute. Students are encouraged to actively participate in National and State level co-curricular and extracurricular activities. The institute has well planned boys and girls hostel in the campus with better amenities and ultra-modern facilities

ABOUT THE CONFERENCE

The DYPCOE is organizing IEEE International Conference on Electronics, Computing and Communication Networking Automation Technologies 2025 to provide an international open forum for educators, engineers and researchers to disseminate their latest research work and exchange views on the future research directions ICEC2NT will be held at Pune, Maharashtra, India on 4th and 5th September 2025 with a focus to target the research on emerging technologies. ICEC2NT will be a blended version of conference wherein online as well as offline presentations mode will be there guidelines.

Important Dates

Paper Submission : 10 May 2025 Acceptance Notification : 12 June 2025 Author Registration : 18 June 2025 Conference : 3 - 4 September 2025

Call for Papers

- Electronics
- Communication
- Industry 4.0, Control and Automation
- Networking
- Computing Technologies

Submission Gudielines :

Prospective authors are encouraged to submit high-quality original, previously unpublished, experimental or theoretical research papers for presentation at the Conference. Articles submitted to the conference should meet these criteria and must not be under consideration for publication elsewhere.

The paper should be in the format strictly as per the Paper Template given below. The papers should be drafted in English with a maximum of Eight (8) pages and a minimum of 4 pages including figures, tables, and references. The paper will be peer-reviewed by domain experts of the respective tracks by the Technical Program Committee. Authors should submit the papers through CMT Microsoft. Kindly do not submit the paper multiple times; it may lead to cancellation of your paper.

CHIEF PATRON

• Padmashree Hon'ble Dr. D Y Patil Founder President, Dr. D Y Patil Pratishthan, Kolhapur

Patrons

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Dr. Mrs P Malathi

Principal D Y Patil College of Engineering, Akurdi, Pune

REGISTRATION

	INDIAN AUTHORS		
Categories	EDS Members	IEEE Member	Non IEEE Member
Graduate Student/Research Scholar/Academician/Industry	7800₹	8600₹	9600₹
Extra page (after 6 pages)	500₹	500₹	500₹
	FOREIGN AUTHOR	S	
Graduate Student/Research Scholar/Academician/Industry	160\$	180\$	220\$
Extra page (after 6 pages)	10\$	10\$	10\$

SCAN QR Paper Submission Last date for Paper Submission : 30 April 2025 https://cmt3.research.microsoft.com/ICEC2NT2025/

ed in the conference will be submitted for inclusion into the IEEE Xplore Digital Library

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